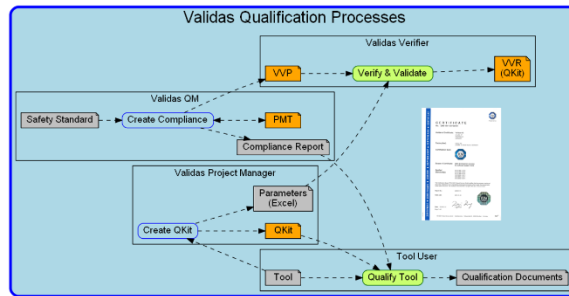


Verification and Validation Report for Perform Module Test



Version:	Template 0.4 / Document 1.0
Date:	2019-03-11
Status:	Generic / Generated / Reviewed / Final
Author:	Dr. Oscar Slotosch (Template), Oscar Slotosch using VVT
File:	VVR.docm
Size:	24 Pages



Template Revision History:

Version	Date	Status	Author	Change
0.1	2018-11-19	Generic	Slotosch	Created initial template
0.2	2018-11-19	Generic	Slotosch	Added Chapter structure and introduction
0.3	2018-01-06	Generic	Slotosch	Added Issues and Project chapters
0.4	2019-03-11	Generic	Slotosch	Updated title page and history

Document Revision History

Version	Date	Status	Author	Change
0.1	Mon 2019/03/11 07:06:53	Generic	Slotosch	Created from generic Template 0.4
0.3	Mon 2019/03/11 07:06:53	Generated	Oscar Slotosch	Generated from VVT after performing V&V
0.5	11.3.2019	Reviewed	Oscar Slotosch	Reviewed Requirements & Compliance
1.0	11.3.2019	Final	Oscar Slotosch	Finalized document

Contents

1	Scope of this Document.....	5
2	Project Perform Module Test	6
3	Parameters of Perform Module Test.....	6
3.1	Parameter MODULE & Values.....	6
4	Persons in Perform Module Test	7
4.1	Person Tester.....	7
5	Artifacts of Perform Module Test	7
5.1	Artifact Code Coverage Report	7
5.2	Artifact SUT.....	8
5.3	Artifact Specification	8
5.4	Artifact Test Case.....	9
5.5	Artifact Test Report	9
6	Checks of Perform Module Test	10
6.1	Check Check at ModuleTest/Artifacts/SafetyCase/CoverageReport.....	10
6.1.1	Check Check of Code Coverage Report	10
6.1.1.1	Check Analyze MCDC.....	11
6.1.1.1.1	Check MCDC OK.....	11
6.1.1.1.2	Check All Conditions Considered.....	12
6.2	Check Check at ModuleTest/Artifacts/SafetyCase/TestReport	13
6.2.1	Check Check of Test Report.....	13
6.2.1.1	Check Analyze Test Results of Test Report	14
6.2.1.1.1	Check Test Results Complete	14
6.2.1.1.2	Check Test Results OK	15
6.3	Check Check at ModuleTest/Artifacts/SafetyCase/Tests.....	16
6.3.1	Check Check of Test Case	16
6.3.1.1	Check Validate Tests.....	17
6.3.1.1.1	Check Test Complete	18
6.3.1.1.2	Check Test Effective.....	18
7	Results for Perform Module Test.....	19
8	Issues of Perform Module Test.....	19
8.1	Fixed Issues in Perform Module Test	19
9	Summary of Perform Module Test.....	20
10	Glossary	21
11	References	23

1 Scope of this Document

This document describes the verification and validation of the project Perform Module Test that has been performed using the process described in [PR]. By successfully validating and verifying the Perform Module Test, the compliance claimed in [CR] for the process is establish.

The applied process is a variant from the generic process and created according to the Validas Qualification Methodology, see [QMeth]. It has been tailored for the project (see Section 2) by assigning values to process parameters (see Section 3) and by defining the requirements to be satisfied see [CR].

The concrete verification and validation (V&V) tasks are described in Section 6. These checks have been performed for all necessary parameters. The responsible persons are documented in Section 4. Every check refers to one artifact that is checked. The artifacts are described in Section 5. The results of this application are described in Section 7. Section 8 lists the issues of the product and Section 9 summarizes the results.

The document concludes with a glossary in Section 10 and references in Section 11.

2 Project Perform Module Test

This chapter describes the general information of the project 'Perform Module Test'.

This V&V report has been generated using compact mode, i.e. it does not contain all details about CheckResults with Result=PASS.

Those details can be found in the VVT model or in the used Excel sheets.

Project Perform Module Test	
Name:	Perform Module Test
Description:	All activities for performing the module tests have been performed successfully. In this example only the coverage measurement is considered (most complex & interesting).
Comment:	2019/03/11 05:11:07: Imported test results from E:\git\pmt-prototype\bundles\de.validas.spm.pmt.examples\ModuleTest\VV\PerformModuleTest_VVReport.xls 2019/03/11 05:16:24: Imported test results from E:\git\pmt-prototype\bundles\de.validas.spm.pmt.examples\ModuleTest\VV\PerformModuleTest_VVReport.xls 2019/03/11 05:28:01: Imported test results from E:\git\pmt-prototype\bundles\de.validas.spm.pmt.examples\ModuleTest\VV\PerformModuleTest_VVReportDelta.xls 2019/03/11 05:42:28: Imported test results from E:\git\pmt-prototype\bundles\de.validas.spm.pmt.examples\ModuleTest\VV\PerformModuleTest_VVReportFinal.xls
Issue Tracker:	https://teststatt.atlassian.net/projects/VP/issues/
Product:	Modules
Version:	Exported 2019/03/10 17:04:33

Table 1 Project Perform Module Test

3 Parameters of Perform Module Test

This chapter describes the configuration of 'Perform Module Test' i.e. the used parameters and their values.

1parameter (and corresponding values) of Perform Module Test are described in this section.

3.1 Parameter MODULE & Values

This section describes parameter MODULE and it's 2 values.

Parameter MODULE	
Name:	MODULE

Description:
The name of module that is currently tested.
Comment:
The name can vary for different modules.

Table 2 Parameter MODULE

The 2 values for MODULE are:

1. math
2. stdio

4 Persons in Perform Module Test

This chapter describes the roles in 'Perform Module Test' i.e. the involved persons and their responsibilities.

4.1 Person Tester

This section describes person Tester and it's activities.

Person Tester
Name:
Tester
Description:
Responsible for testing the module including creation of test specification, implementation analysis.
Assigned Person
Oscar Slotosch
Developed Artifacts:
<ul style="list-style-type: none"> • Code Coverage Report, see Table 4 • Test Case, see Table 7 • Test Report, see Table 8

Table 3 Person Tester

5 Artifacts of Perform Module Test

This chapter describes the artifacts of 'Perform Module Test' i.e. the checked elements.

5.1 Artifact Code Coverage Report

This section describes artifact Code Coverage Report.

Artifact Code Coverage Report
Name:
Code Coverage Report
Description:
The report with the selected coverage (Statement, Branch, MCDC).
Comment:

Input of Analyze MCDC; Verified by Analyze MCDC
Path
ModuleTest/Artifacts/SafetyCase/CoverageReport
Repository
Default Repository
Repository-URL
E:\git\pmt-prototype\bundles\de.validas.spm.pmt.examples\ModuleTest
Modified
11/3/19

Table 4 Artifact Code Coverage Report

5.2 Artifact SUT

This section describes artifact SUT.

Artifact SUT
Name:
SUT
Description:
Subject Under Test, typically SW modules.
Comment:
Input of Analyze MCDC; Input of Analyze Test Results; Input of Validate Tests
Path
ModuleTest/Artifacts/Modules
Repository
Default Repository
Repository-URL
E:\git\pmt-prototype\bundles\de.validas.spm.pmt.examples\ModuleTest
Modified
11/3/19

Table 5 Artifact SUT

5.3 Artifact Specification

This section describes artifact Specification.

Artifact Specification
Name:
Specification
Description:
Functional Specification.
Comment:
Input of Analyze MCDC; Input of Analyze Test Results; Input of Validate Tests
Path
ModuleTest/Artifacts/Specification
Repository
Default Repository

Repository-URL
E:\git\pmt-prototype\bundles\de.validas.spm.pmt.examples\ModuleTest
Modified
11/3/19

Table 6 Artifact Specification

5.4 Artifact Test Case

This section describes artifact Test Case.

Artifact Test Case
Name:
Test Case
Description:
A module test case (implementation).
Comment:
Input of Validate Tests; Verified by Validate Tests
Path
ModuleTest/Artifacts/SafetyCase/Tests
Repository
Default Repository
Repository-URL
E:\git\pmt-prototype\bundles\de.validas.spm.pmt.examples\ModuleTest
Modified
3/10/19

Table 7 Artifact Test Case

5.5 Artifact Test Report

This section describes artifact Test Report.

Artifact Test Report
Name:
Test Report
Description:
The test report created from test execution including the test result (PASS/FAIL).
Comment:
Input of Analyze Test Results; Verified by Analyze Test Results
Path
ModuleTest/Artifacts/SafetyCase/TestReport
Repository
Default Repository
Repository-URL
E:\git\pmt-prototype\bundles\de.validas.spm.pmt.examples\ModuleTest
Modified
11/3/19

Table 8 Artifact Test Report

6 Checks of Perform Module Test

This chapter describes all 15 checks of 'Perform Module Test' i.e. verification tasks.

6.1 Check Check at ModuleTest/Artifacts/SafetyCase/CoverageReport

This section describes check Check at ModuleTest/Artifacts/SafetyCase/CoverageReport and it's 1 contained check (total number of checks in this section is 5).

Check Check at ModuleTest/Artifacts/SafetyCase/CoverageReport	
Name:	Check at ModuleTest/Artifacts/SafetyCase/CoverageReport
Description:	Checking 1 Artifact in ModuleTest/Artifacts/SafetyCase/CoverageReport. Once all sub-checks are performed successfully, this implicit check can be set to PASS.
Comment:	If ModuleTest/Artifacts/SafetyCase/CoverageReport changes all 1 check has to be repeated. VVT (2019/03/11 05:21:44):Analyzed new Verdict FAILED of Check at ModuleTest/Artifacts/SafetyCase/CoverageReport (CREATED):has one sub-check:FAILED VVT (2019/03/11 05:28:28):Analyzed new Verdict PASSED of Check at ModuleTest/Artifacts/SafetyCase/CoverageReport (FAILED):has one sub-check:PASSED
Verdict	PASSED
Sub Check:	<ul style="list-style-type: none">• Check of Code Coverage Report, see Table 10

Table 9 Check Check at ModuleTest/Artifacts/SafetyCase/CoverageReport

6.1.1 Check Check of Code Coverage Report

This section describes check Check of Code Coverage Report and it's 1 contained check (total number of checks in this section is 4).

Check Check of Code Coverage Report	
Name:	Check of Code Coverage Report
Description:	Checking 1 check for Artifact Code Coverage Report. Once all sub-checks are performed successfully, this implicit check can be set to PASS.
Comment:	If ModuleTest/Artifacts/SafetyCase/CoverageReport changes all 1 check has to be repeated. VVT (2019/03/11 05:21:44):Analyzed new Verdict FAILED of Check of Code Coverage Report (ASSIGNED):has one sub-check:FAILED VVT (2019/03/11 05:28:28):Analyzed new Verdict PASSED of Check of Code Coverage Report (FAILED):has one sub-check:PASSED
Verdict	

PASSED
Author:
<ul style="list-style-type: none"> Tester, see Table 3
Checked Artifact
Code Coverage Report, see Table 4
Sub Check:
<ul style="list-style-type: none"> Analyze MCDC, see Table 11

Table 10 Check Check of Code Coverage Report

6.1.1.1 Check Analyze MCDC

This section describes check Analyze MCDC and it's 2 contained checks.

Check Analyze MCDC
Name:
Analyze MCDC
Description:
Analyze if MCDC is 100% or if there are explained reasons why the coverage is <100%.
Comment:
null VVT (2019/03/11 05:21:44):Analyzed new Verdict FAILED of Analyze MCDC (ASSIGNED):has 2 sub-checks (worst=FAILED):1 FAILED, 1 PASSED VVT (2019/03/11 05:28:28):Analyzed new Verdict PASSED of Analyze MCDC (FAILED):has 2 sub-checks (worst=PASSED):2 PASSED
Verdict
PASSED
Author:
<ul style="list-style-type: none"> Tester, see Table 3
Checked Artifact
Code Coverage Report, see Table 4
Required Artifacts:
<ul style="list-style-type: none"> Code Coverage Report, see Table 4 SUT, see Table 5 Specification, see Table 6
Parameter:
<ul style="list-style-type: none"> MODULE, see Table 2
Sub Checks:
<ul style="list-style-type: none"> MCDC OK [AnaCCR-MC-C1], see Table 12 All Conditions Considered [AnaCCR-MC-C2], see Table 13

Table 11 Check Analyze MCDC

6.1.1.1.1 Check MCDC OK

This section describes check MCDC OK.

Check MCDC OK
Name:
MCDC OK

ID:
AnaCCR-MC-C1
Description:
Is the MCDC 100% (or are reasonable explanations given in case it is less than 100%)?
Comment:
null VVT (2019/03/11 05:21:44):Analyzed new Verdict PASSED of MCDC OK (ASSIGNED):has 2 results (worst=PASSED):2 PASSED
Verdict
PASSED
Author:
<ul style="list-style-type: none"> Tester, see Table 3
Checked Artifact
Code Coverage Report, see Table 4
Parameter:
<ul style="list-style-type: none"> MODULE, see Table 2
Instances:
<ul style="list-style-type: none"> MODULE=math MODULE=stdio
Contained Check Results:
<ul style="list-style-type: none"> MCDC OK MODULE=math OK(PASSED) MCDC OK MODULE=stdio OK(PASSED)

Table 12 Check MCDC OK

6.1.1.1.2 Check All Conditions Considered

This section describes check All Conditions Considered.

Check All Conditions Considered	
Name:	All Conditions Considered
ID:	AnaCCR-MC-C2
Description:	Have all conditions of the module been considered / instrumented (or are some files not instrumented that belong to the module)?
Comment:	null VVT (2019/03/11 05:21:44):Analyzed new Verdict FAILED of All Conditions Considered (ASSIGNED):has 2 results (worst=FAILED):1 FAILED, 1 PASSED VVT (2019/03/11 05:28:28):Analyzed new Verdict PASSED of All Conditions Considered (FAILED):has 2 results (worst=PASSED):2 PASSED
Verdict	PASSED
Author:	<ul style="list-style-type: none"> Tester, see Table 3
Checked Artifact	

Code Coverage Report, see Table 4
Parameter:
<ul style="list-style-type: none"> MODULE, see Table 2
Instances:
<ul style="list-style-type: none"> MODULE=math MODULE=stdio
Contained Check Results:
<ul style="list-style-type: none"> All Conditions Considered MODULE=math OK(PASSED) All Conditions Considered MODULE=stdio OK(PASSED)

Table 13 Check All Conditions Considered

6.2 Check Check at ModuleTest/Artifacts/SafetyCase/TestReport

This section describes check Check at ModuleTest/Artifacts/SafetyCase/TestReport and it's 1 contained check (total number of checks in this section is 5).

Check Check at ModuleTest/Artifacts/SafetyCase/TestReport
Name:
Check at ModuleTest/Artifacts/SafetyCase/TestReport
Description:
Checking 1 Artifact in ModuleTest/Artifacts/SafetyCase/TestReport. Once all sub-checks are performed successfully, this implicit check can be set to PASS.
Comment:
If ModuleTest/Artifacts/SafetyCase/TestReport changes all 1 check has to be repeated. VVT (2019/03/11 05:21:44):Analyzed new Verdict PASSED of Check at ModuleTest/Artifacts/SafetyCase/TestReport (CREATED):has one sub-check:PASSED
Verdict
PASSED
Sub Check:
<ul style="list-style-type: none"> Check of Test Report, see Table 15

Table 14 Check Check at ModuleTest/Artifacts/SafetyCase/TestReport

6.2.1 Check Check of Test Report

This section describes check Check of Test Report and it's 1 contained check (total number of checks in this section is 4).

Check Check of Test Report
Name:
Check of Test Report
Description:
Checking 1 check for Artifact Test Report. Once all sub-checks are performed successfully, this implicit check can be set to PASS.
Comment:
If ModuleTest/Artifacts/SafetyCase/TestReport changes all 1 check has to be repeated. VVT (2019/03/11 05:21:44):Analyzed new Verdict PASSED of Check of Test Report (ASSIGNED):has one sub-check:PASSED

Verdict
PASSED
Author:
<ul style="list-style-type: none"> Tester, see Table 3
Checked Artifact
Test Report, see Table 8
Sub Check:
<ul style="list-style-type: none"> Analyze Test Results of Test Report, see Table 16

Table 15 Check Check of Test Report

6.2.1.1 Check Analyze Test Results of Test Report

This section describes check Analyze Test Results of Test Report and it's 2 contained checks.

Check Analyze Test Results of Test Report	
Name:	Analyze Test Results of Test Report
Description:	Checks the test results (There should not be surprises and all expected tests should be OK). It also contributes to the coveral sumary statement on the safety of the tested module(s).
Comment:	Analysis of test results is done as part of the process (and it is required for unit tests), but it is not required for coverage (6.9.4.4) VVT (2019/03/11 05:21:44):Analyzed new Verdict PASSED of Analyze Test Results of Test Report (ASSIGNED):has 2 sub-checks (worst=PASSED):2 PASSED
Verdict	PASSED
Author:	<ul style="list-style-type: none"> Tester, see Table 3
Checked Artifact	Test Report, see Table 8
Required Artifacts:	<ul style="list-style-type: none"> SUT, see Table 5 Specification, see Table 6 Test Report, see Table 8
Parameter:	<ul style="list-style-type: none"> MODULE, see Table 2
Sub Checks:	<ul style="list-style-type: none"> Test Results Complete [AnaTR-C1], see Table 17 Test Results OK [AnaTR-C2], see Table 18

Table 16 Check Analyze Test Results of Test Report

6.2.1.1.1 Check Test Results Complete

This section describes check Test Results Complete.

Check Test Results Complete

Name:
Test Results Complete
ID:
AnaTR-C1
Description:
Are the test results complete, i.e. have all required tests been executed?
Comment:
null VVT (2019/03/11 05:21:44):Analyzed new Verdict PASSED of Test Results Complete (ASSIGNED):has 2 results (worst=PASSED):2 PASSED
Verdict
PASSED
Author:
<ul style="list-style-type: none"> Tester, see Table 3
Checked Artifact
Test Report, see Table 8
Parameter:
<ul style="list-style-type: none"> MODULE, see Table 2
Instances:
<ul style="list-style-type: none"> MODULE=math MODULE=stdio
Contained Check Results:
<ul style="list-style-type: none"> Test Results Complete MODULE=math OK(PASSED) Test Results Complete MODULE=stdio OK(PASSED)

Table 17 Check Test Results Complete

6.2.1.1.2 Check Test Results OK

This section describes check Test Results OK.

Check Test Results OK	
Name:	
Test Results OK	
ID:	
AnaTR-C2	
Description:	
Are the tests results OK, i.e. PASS or only failing/error for known bugs?	
Comment:	
null VVT (2019/03/11 05:21:44):Analyzed new Verdict PASSED of Test Results OK (ASSIGNED):has 2 results (worst=PASSED):2 PASSED	
Verdict	
PASSED	
Author:	
<ul style="list-style-type: none"> Tester, see Table 3 	
Checked Artifact	

Test Report, see Table 8
Parameter:
<ul style="list-style-type: none"> MODULE, see Table 2
Instances:
<ul style="list-style-type: none"> MODULE=math MODULE=stdio
Contained Check Results:
<ul style="list-style-type: none"> Test Results OK MODULE=math OK(PASSED) Test Results OK MODULE=stdio OK(PASSED)

Table 18 Check Test Results OK

6.3 Check Check at ModuleTest/Artifacts/SafetyCase/Tests

This section describes check Check at ModuleTest/Artifacts/SafetyCase/Tests and it's 1 contained check (total number of checks in this section is 5).

Check Check at ModuleTest/Artifacts/SafetyCase/Tests	
Name:	Check at ModuleTest/Artifacts/SafetyCase/Tests
Description:	Checking 1 Artifact in ModuleTest/Artifacts/SafetyCase/Tests. Once all sub-checks are performed successfully, this implicit check can be set to PASS.
Comment:	<p>If ModuleTest/Artifacts/SafetyCase/Tests changes all 1 check has to be repeated.</p> <p>VVT (2019/03/11 05:21:44):Analyzed new Verdict FAILED of Check at ModuleTest/Artifacts/SafetyCase/Tests (CREATED):has one sub-check:FAILED</p> <p>VVT (2019/03/11 05:28:28):Analyzed new Verdict PASSED of Check at ModuleTest/Artifacts/SafetyCase/Tests (FAILED):has one sub-check:PASSED</p>
Verdict	PASSED
Sub Check:	<ul style="list-style-type: none"> Check of Test Case, see Table 20

Table 19 Check Check at ModuleTest/Artifacts/SafetyCase/Tests

6.3.1 Check Check of Test Case

This section describes check Check of Test Case and it's 1 contained check (total number of checks in this section is 4).

Check Check of Test Case	
Name:	Check of Test Case
Description:	Checking 1 check for Artifact Test Case. Once all sub-checks are performed successfully, this implicit check can be set to PASS.
Comment:	If ModuleTest/Artifacts/SafetyCase/Tests changes all 1 check has to be repeated.

VVT (2019/03/11 05:21:44):Analyzed new Verdict FAILED of Check of Test Case (ASSIGNED):has one sub-check:FAILED
VVT (2019/03/11 05:28:28):Analyzed new Verdict PASSED of Check of Test Case (FAILED):has one sub-check:PASSED
Verdict
PASSED
Author:
<ul style="list-style-type: none"> Tester, see Table 3
Checked Artifact
Test Case, see Table 7
Sub Check:
<ul style="list-style-type: none"> Validate Tests, see Table 21

Table 20 Check Check of Test Case

6.3.1.1 Check Validate Tests

This section describes check Validate Tests and it's 2 contained checks.

Check Validate Tests
Name:
Validate Tests
Description:
Tests can be validated in many ways, typically by compiling them, eventually by testing against a standard implementations from open source or reviewing them. Validas.
Comment:
null VVT (2019/03/11 05:21:44):Analyzed new Verdict FAILED of Validate Tests (ASSIGNED):has 2 sub-checks (worst=FAILED):1 FAILED, 1 PASSED VVT (2019/03/11 05:28:28):Analyzed new Verdict PASSED of Validate Tests (FAILED):has 2 sub-checks (worst=PASSED):2 PASSED
Verdict
PASSED
Author:
<ul style="list-style-type: none"> Tester, see Table 3
Checked Artifact
Test Case, see Table 7
Required Artifacts:
<ul style="list-style-type: none"> SUT, see Table 5 Specification, see Table 6 Test Case, see Table 7
Parameter:
<ul style="list-style-type: none"> MODULE, see Table 2
Sub Checks:
<ul style="list-style-type: none"> Test Complete [VT-C1], see Table 22 Test Effective [VT-C2], see Table 23

Table 21 Check Validate Tests

6.3.1.1.1 Check Test Complete

This section describes check Test Complete.

Check Test Complete	
Name:	
	Test Complete
ID:	
	VT-C1
Description:	
	Does the test cover the requirement completely?
Comment:	
	null VVT (2019/03/11 05:21:44):Analyzed new Verdict PASSED of Test Complete (ASSIGNED):has 2 results (worst=PASSED):2 PASSED
Verdict	
	PASSED
Author:	
	<ul style="list-style-type: none">• Tester, see Table 3
Checked Artifact	
	Test Case, see Table 7
Parameter:	
	<ul style="list-style-type: none">• MODULE, see Table 2
Instances:	
	<ul style="list-style-type: none">• MODULE=math• MODULE=stdio
Contained Check Results:	
	<ul style="list-style-type: none">• Test Complete MODULE=math OK(PASSED)• Test Complete MODULE=stdio OK(PASSED)

Table 22 Check Test Complete

6.3.1.1.2 Check Test Effective

This section describes check Test Effective.

Check Test Effective	
Name:	
	Test Effective
ID:	
	VT-C2
Description:	
	Can the test detect deviations (failures/errors) in case they would occur, e.g. by using some statements like "assert"?
Comment:	
	null VVT (2019/03/11 05:21:44):Analyzed new Verdict FAILED of Test Effective (ASSIGNED):has 2 results (worst=FAILED):1 FAILED, 1 PASSED

VVT (2019/03/11 05:28:28):Analyzed new Verdict PASSED of Test Effective (FAILED):has 2 results (worst=PASSED):2 PASSED
Verdict
PASSED
Author:
<ul style="list-style-type: none"> Tester, see Table 3
Checked Artifact
Test Case, see Table 7
Parameter:
<ul style="list-style-type: none"> MODULE, see Table 2
Instances:
<ul style="list-style-type: none"> MODULE=math MODULE=stdio
Contained Check Results:
<ul style="list-style-type: none"> Test Effective MODULE=math OK(PASSED) Test Effective MODULE=stdio OK(PASSED)

Table 23 Check Test Effective

7 Results for Perform Module Test

This chapter describes 12 results (OK:12, NOK:0) for 'Perform Module Test' i.e. the available verification results from performing the checks, described in the previous chapter.

In compact mode only the 0 failing result are listed, i.e. all other 12 results are OK and can be found in the VVT model or the excel sheets

Since there are no failing results, this chapter remains empty in compact mode.

8 Issues of Perform Module Test

This chapter describes the relevant (2) issues of the project 'Perform Module Test'.

8.1 Fixed Issues in Perform Module Test

This section describes the 2 fixed issues.

Issue Issue number #17
Name:
Issue number #17
ID:
#17
Description:
stdio.h not instrumented. stdi.h contains code with conditions and has not been instrumented, such that MCDC coverage cannot be determined.
Workaround:
manually review/re-check coverage in pre-processed stdio.h
Found In revision Or Date:
10-Mar
Fixed In revision Or Date:

Table 24 Issue Issue number #17

Issue Issue number #18	
Name:	
Issue number #18	
ID:	
#18	
Description:	
side effects of printf not tested. printf has side effect, e.g. on stdout that have not been tested.	
Workaround:	
review side effects or do not use printf functions	
Found In revision Or Date:	
10-Mar	
Fixed In revision Or Date:	
11-Mar	

Table 25 Issue Issue number #18

9 Summary of Perform Module Test

This section describes the summary of the Verification and Validation activities for 'Perform Module Test'.

15 checks have been performed with 12 instances.

All 27 checks and instances have been performed SUCCESSFULLY, hence Perform Module Test is successfully verified and validated and complies with it's requirements.

The following 27 checks and instances have been performed successfully:

1. Check MCD C OK [AnaCCR-MC-C1]:PASSED, see Table 12
2. Check Result MCD C OK MODULE=math OK:PASSED
3. Check Result MCD C OK MODULE=stdio OK:PASSED
4. Check All Conditions Considered [AnaCCR-MC-C2]:PASSED, see Table 13
5. Check Result All Conditions Considered MODULE=stdio OK:PASSED
6. Check Result All Conditions Considered MODULE=math OK:PASSED
7. Check Test Results Complete [AnaTR-C1]:PASSED, see Table 17
8. Check Result Test Results Complete MODULE=math OK:PASSED
9. Check Result Test Results Complete MODULE=stdio OK:PASSED
10. Check Test Results OK [AnaTR-C2]:PASSED, see Table 18
11. Check Result Test Results OK MODULE=stdio OK:PASSED
12. Check Result Test Results OK MODULE=math OK:PASSED
13. Check Analyze MCD C: PASSED, see Table 11
14. Check Analyze Test Results of Test Report: PASSED, see Table 16
15. Check Check at ModuleTest/Artifacts/SafetyCase/CoverageReport: PASSED, see Table 9
16. Check Check at ModuleTest/Artifacts/SafetyCase/TestReport: PASSED, see Table 14
17. Check Check at ModuleTest/Artifacts/SafetyCase/Tests: PASSED, see Table 19
18. Check Check of Code Coverage Report: PASSED, see Table 10

19. Check Check of Test Case:PASSED, see Table 20
20. Check Check of Test Report:PASSED, see Table 15
21. Check Test Complete [VT-C1]:PASSED, see Table 22
22. Check Result Test Complete MODULE=math OK:PASSED
23. Check Result Test Complete MODULE=stdio OK:PASSED
24. Check Test Effective [VT-C2]:PASSED, see Table 23
25. Check Result Test Effective MODULE=stdio OK:PASSED
26. Check Result Test Effective MODULE=math OK:PASSED
27. Check Validate Tests:PASSED, see Table 21

10 Glossary

The following abbreviations are used in the document. More information on the concepts & processes can be found in [QMeth].

- AOC: Anomalous Operating Condition
- CR: Compliance Report¹
- CT: Construction Task (during QKit creation)
- KB: Known Bug
- LCR: Library Classification Report
- LQP: Library Qualification Plan
- LQR: Library Qualification Report
- LSM: Library Safety Manual
- LTG: Library Test Generator
- PCCP: (Development) Process Compliance Check Plan
- PCCR: (Development) Process Compliance Check Report
- PMT: Process Modeling Tool
- PT: Preparation Task (before QKit creation)
- QKit: Qualification Kit
- QP: Qualification Plan (general), can be LQP or TQP
- QR: Qualification Report (general), can be LQR or TQR
- QST: Qualification Support Tool
- SEOOO: Safety Element Out Of Context according to [ISO26262]
- SM: Safety Manual (general), can be LSM or TSM
- SWC: Software Component, e.g. a library²
- TAU: Test Automation Unit
- TCA: Tool Chain Analyzer
- TD: Tool Detection (part of TCL computation according to [ISO26262])
- TCL: Tool Confidence Level (according to [ISO26262])
- TCR: Tool Classification Report
- TI: Tool Impact (part of TCL computation according to [ISO26262])
- TQL: Tool Qualification Level (according to [DO330])
- TQP: Tool Qualification Plan

¹ Do not confuse with Classification Reports LCR and TCR.

² Note that libraries can be both changes and unchanged software components.

- TQR: Tool Qualification Report
- TSM: Tool Safety Manual
- V&V: Verification and Validation
- VVP: Verification and Validation Plan
- VVR: Verification and Validation Report
- VVT: Verification and Validation Tool
- VT: Verification task (after QKit creation)

11 References

- [CR] Compliance Report for the Process of Perform Module Test, generated by PMT
- [DO330] RTCA. DO-330: Software Tool Qualification Considerations 1st Edition 2011-12-13.
- [DO178C] RTCA. DO-178C: Software Considerations in Airborne Systems and Equipment Certification, 2011-12-13.
- [EN50128] BS EN 50128:2011, Railway applications — Communication, signaling and processing systems — Software for railway control and protection systems, BSI Standards Publication
- [FDA2002] General Principles of Software Validation; Final Guidance for Industry and FDA Staff, Jan 2002, from <http://www.fda.gov/downloads/MedicalDevices/DeviceRegulationandGuidance/GuidanceDocuments/ucm085371.pdf>
- [FDA_OTs] Guidance for Industry, FDA Reviewers and Compliance on Off-The-Shelf Software Use in Medical Devices, Center for Devices and Radiological Health (CDRH), from <http://www.fda.gov/downloads/MedicalDevices/DeviceRegulationandGuidance/GuidanceDocuments/ucm073779.pdf>
- [IEC61508] International Electrotechnical Commission, IEC 61508, Functional safety of electrical/electronic/programmable electronic safety-related systems, Edition 2.0, Apr 2010.
- [IEC62304] International Electrotechnical Commission, IEC 62304, Medical device software – Software life cycle processes
- [ISO26262] International Organization for Standardization, ISO 26262 Road Vehicles – Functional safety– 1st Edition, 2011-11-15.
- [PR] Process Report for Process of Perform Module Test, generated by PMT
- [QMeth] Validas Qualification Method, White Paper, Version 1.7, see <Documentation>/QualificationMethodology.pdf.
- [VVP] Verification and Validation Plan (Model) for Perform Module Test generated by PMT